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Docket No.: URI.5473

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Uht et al.

GROUP: Unknown

SERIAL NO: 09/828,600

EXAMINER: Unknown

FILED: 04/06/2001

FOR: RESOURCE FLOW COMPUTING DEVICE

Box Missing Parts
Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

FIRST RELIMINARY AMENDMENT

Please amend the application as follows:

IN THE ABSTRACT:

Please add the following abstract to the application:

--ABSTRACT

ad
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A scalable processing system includes a memory device having a plurality of executable program instructions, wherein each of the executable program instructions includes a timetag data field indicative of the nominal sequential order of the associated executable program instructions. The system also includes a plurality of processing elements, which are configured and arranged to receive executable program instructions from the memory device, wherein each of the processing elements executes executable instructions having the highest priority as indicated by the state of the timetag data field.--

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231, Attn: Application Division.

Amy M. Flick

7-18-2001

Date